**Donald C. Mayer** is currently the Director of the Space Electronics Vulnerability Office at The Aerospace Corporation in El Segundo, CA. He joined The Aerospace Corporation in 1989, where he has worked on advanced microelectronics technologies, including CMOS, CMOS-on-insulator, and microelectromechanical systems (MEMS) with emphasis on their reliability and radiation hardness for space applications. He currently directs activities related to electronics and optoelectronics vulnerabilities in space, as well as mission assurance activities for DOD, civil, and commercial space programs.

From 1977 to 1989, he worked at the Hughes Research Laboratories in Malibu, CA, where he helped to develop a submicrometer CMOS-on-sapphire process technology, including electron-beam and ion-beam lithography, and CMOS integrated circuit design and verification techniques. Since 1985, he has taught digital integrated circuit design and circuit analysis courses as a Visiting Lecturer at UCLA.

He has served on the Technical Program Committee of the IEEE GaAs IC Symposium and the Technical and Executive Committees of the IEEE International Silicon-on-Insluator (SOI) Conference, most recently as the 1996 SOI Conference General Chair. He helped organize the Autonomous Vehicle Technology Showcase at WESCON 2004 and the 2005 IEEE Nanotechnology in Aerospace and Electronics Conference.

He has authored or coauthored over sixty papers and conference presentations, and has been awarded six patents. He received the 1997 IEEE Nuclear and Space Radiation Effects (NSREC) Meritorious Paper Award, and the 1998 NSREC Outstanding Paper Award.

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